

No. of Printed Pages : 3

Roll No.

18C2

B.Tech. EXAMINATION, 2024

(Third Semester)

(C Scheme) (Main & Re-appear)

(CSE, ECE)

CSE203C

COMPUTER ORGANIZATION AND
ARCHITECTURE

Time : 3 Hours]

[Maximum Marks : 75

Before answering the question-paper candidates should ensure that they have been supplied to correct and complete question-paper. No complaint, in this regard, will be entertained after the examination.

Note : Attempt *Five* questions in all, selecting at least *one* question from each Unit. All questions carry equal marks.

Unit I

1. (a) Explain Synchronous and Asynchronous bus clocking. 7
- (b) Construct a bus system for 4 bit register. 8
2. Construct a 4 bit ripple carry adder and carry look-ahead adder. 15

Unit II

3. Explain different types of addressing modes. 15
4. (a) Explain Instruction cycle. 8
- (b) What is control flow ? Explain with suitable example. 7

Unit III

5. Explain designing of control unit by using hardwired control. 15

6. What is address sequencer ? What is the role of address sequencer in CPU ? 15

Unit IV

7. Explain the various types of memory. 15
8. Explain Page replacement algorithm. 15